ABSTRACT

A deterministic transmission process for asynchronous data in packets stores data arriving in an asynchronous manner in registers as and when it arrives. The data is sent to one of several packeting modules, which conduct start of packeting, packeting with sorting and enhancement of data, end of the packeting, and sending of the made-up or assembled packets to a message composition module at the request of the message composition module during the realization cycle of the packet, so that a new realization cycle is commenced. The packets are recovered one after another, in a predefined order, in the message composition module. The message thus composed is set in the electrical format in the protocol used for transmission.